

SERIAL NUMBER (Series of 1987) 07/863791	PATENT DATE	PATENT NUMBER
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SERIAL NUMBER 07/863,791	FILING DATE 04/06/92	CLASS 436	SUBCLASS 312	GROUP ART UNIT 1507 1503	EXAMINER <i>Knight</i> <i>Dodson</i> <i>Mosley</i>
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CONTINUING DATA***

VERIFIED

SAD

FOREIGN/PCT APPLICATIONS***

VERIFIED

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FOREIGN FILING LICENSE GRANTED 05/06/92

Foreign priority claimed 35 USC 119 conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no	AS FILED	STATE OR COUNTRY CA	SHEETS DRWGS. 7	TOTAL CLAIMS 23	INDEP. CLAIMS 3	FILING FEE RECEIVED \$1,010.00	ATTORNEY'S DOCKET NO. 57017.P025
Verified and Acknowledged	Examiner's Initials <i>SAD</i>							

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TITLE METHOD FOR IMPROVED LITHOGRAPHIC PATTERNING IN A SEMICONDUCTOR FABRICATION PROCESS

U.S. DEPT. of COMM. Pat. & TM Office — PTO-436L (rev. 10-78)

PARTS OF APPLICATION
FILED SEPARATELY

NOTICE OF ALLOWANCE MAILED		PREPARED FOR ISSUE		CLAIMS ALLOWED	
		Assistant Examiner	Docket Clerk	Total Claims	Print Claim
ISSUE FEE		Primary Examiner		DRAWING	
Amount Due	Date Paid			Sheets Drwg.	Figs. Drwg.
Label Area		ISSUE CLASSIFICATION		ISSUE BATCH NUMBER	
		Class	Subclass		
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